



## UNITED STATES DEPARTMENT OF COMMERCE Patent and Trademark Offic

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR			ATTORNEY DOCKET NO.
9/867,836	05/30/01	HARARI		E :	11587 M-1018
-			$\neg$	EXAMINER	
		MMC2/0910	•		
ERALD P. PARSONS				TRAN, A	
KJER VEN MORRILL MACPHERSON LLP				ART UNIT	PAPER NUMBER
NITE 2800					
HREE EMBARCADERO CENTER				2824	)
N FRANCISCO	1 CA 94111			DATE MAILED	
				DATE MAILED	09/10/01

Please find below and/or attached an Office communication concerning this application or proceeding.

**Commissioner of Patents and Trademarks** 





Application No. 09/867,836

Applicant(s)

ELIYAHOU HARARI et al.

## Office Action Summary

Examiner

Andrew Q. Tran

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The MAILING DATE of this communication appears	on the cover sheet with the correspondence address				
Period for Reply  A SHORTENED STATUTORY PERIOD FOR REPLY IS SET	TO EXPIRE 1 MONTH(S) FROM				
THE MAILING DATE OF THIS COMMUNICATION.					
<ul> <li>Extensions of time may be available under the provisions of 37 Cl after SIX (6) MONTHS from the mailing date of this communic</li> <li>If the period for reply specified above is less than thirty (30) days be considered timely.</li> </ul>	ation.				
<ul> <li>If NO period for reply is specified above, the maximum statutory period communication.</li> </ul>	period will apply and will expire SIX (6) MONTHS from the mailing date of this statute, cause the application to become ABANDONED (35 U.S.C. § 133).				
<ul> <li>- Pailure to reply within the set of extended period for reply will, by</li> <li>- Any reply received by the Office later than three months after the earned patent term adjustment. See 37 CFR 1.704(b).</li> </ul>	mailing date of this communication, even if timely filed, may reduce any				
Status					
1) Responsive to communication(s) filed on <u>May 30, 2</u>	2001				
2a) ☐ This action is <b>FINAL</b> . 2b) ☐ This act	ion is non-final.				
3) Since this application is in condition for allowance of closed in accordance with the practice under Ex pa					
Disposition of Claims					
4) 💢 Claim(s) <u>63-94</u>	is/are pending in the application.				
	is/are withdrawn from consideration.				
5) Claim(s)	is/are allowed.				
6) Claim(s)	is/are rejected.				
7) Claim(s)	is/are objected to.				
8) 💢 Claims <u>63-94</u>	are subject to restriction and/or election requirement.				
Application Papers					
9) The specification is objected to by the Examiner.					
10) The drawing(s) filed on is/are	objected to by the Examiner.				
11) The proposed drawing correction filed on	is: a)□ approved b)□ disapproved.				
12) $\square$ The oath or declaration is objected to by the Exam	iner.				
Priority under 35 U.S.C. § 119					
13) Acknowledgement is made of a claim for foreign p	riority under 35 U.S.C. § 119(a)-(d).				
a) $\square$ All b) $\square$ Some* c) $\square$ None of:					
1. Certified copies of the priority documents have	re been received.				
2. Certified copies of the priority documents have	re been received in Application No				
3. Copies of the certified copies of the priority d application from the International Bure *See the attached detailed Office action for a list of the					
14) Acknowledgement is made of a claim for domestic					
Attachment(s)					
15) Notice of References Cited (PTO-892)	18) Interview Summary (PTO-413) Paper No(s).				
16) Notice of Draftsperson's Patent Drawing Review (PTO-948)	19) Notice of Informal Patent Application (PTO-152)				
17) Information Disclosure Statement(s) (PTO-1449) Paper No(s) 20) Other:					
<del></del>					

Application/Control Number: 09/867,836

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The Preliminary Amendment filed May 30, 2001 has been received and placed of record.

Claims 1-62 been canceled.

Claims 63-94 been added.

Claims 63-94 pending.

Restriction to one of the following inventions is required under 35 U.S.C. 121:

- I. Claims 63-75 and 77-78, drawn to a non-volatile memory system for programming and program verifying multi-level memory cells, classified in class 365, subclass 185.03.
- II. Claims 76 and 79-80, drawn to a non-volatile memory system for programming and program verifying two-level memory cells, classified in class 365, subclass 185.22.
- III. Claims 81-82, 84, 86-90 and 93, drawn to a method for programming and program verifying two-level memory cells, classified in class 365, subclass 185.24.
- IV. Claims 83, 85, 91-92 and 94, drawn to a method for programming and program verifying multi-level memory cells, classified in class 365, subclass 185.11.

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The inventions are distinct, each from the other because:

Inventions of Group I and Group IV are related as product and process of use. The inventions can be shown to be distinct if either or both of the following can be shown: (1) the process for using the product as claimed can be practiced with another materially different product or (2) the product as claimed can be used in a materially different process of using that product (MPEP § 806.05(h)). In the instant case, the method for programming and program verifying multi-level memory cells of the invention of Group IV, as claimed, can be practiced with another materially different product, such as other multi-level memory cell system known in the art, other than the multi-level non-volatile memory system of the invention of Group I.

Inventions of Group II and Group III are related as product and process of use. The inventions can be shown to be distinct if either or both of the following can be shown: (1) the process for using the product as claimed can be practiced with another materially different product or (2) the product as claimed can be used in a materially different process of using that product (MPEP § 806.05(h)). In the instant case, the method for

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programming and program verifying two-level memory cells of the invention of Group III, as claimed, can be practiced with another materially different product, such as other two-level memory cell system known in the art, other than the two-level non-volatile memory system of the invention of Group II.

The Group I invention is distinct from that of Group II because Group I recites a multi-level memory cell system; while Group II claims a two-level memory cell system.

On the other hand, the Group III invention is distinct from that of Group IV because Group III draws to a method for programming and program verifying two-level memory cells; while Group IV directs to a method for programming and program verifying multi-level memory cells.

Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.

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Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).

Papers related to this application may be submitted to Technology Center 2800, Group 2810 by facsimile transmission. Papers should be faxed to Group 2810 via the Fax Center. The faxing of such papers must conform with the notice published in the Official Gazette, 1096 OG 30 (October 19, 1988). The Fax Center number is (703) 308-7722 or (703) 308-7724.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Andrew Q. Tran whose telephone number is (703) 305-3495.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (703) 308-0956.

Andrew Q. Tran September 06, 2001

> ANDREW Q.TRAN PRIMARY EXAMINER